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JAN 09 2004

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant Craig T. Salling et al.

Docket Number: TI-32536

Serial No.: 10/051,639

Art Unit: 2815

Filed: 01/18/02

Examiner: Matthew C. Landau

For: ESD Improvement by a Vertical Bipolar Transistor with  
Low Breakdown Voltage and High Beta

OFFICIAL

## CERTIFICATION OF FACSIMILE TRANSMISSION

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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	<input checked="" type="checkbox"/> LETTER
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Craig T. Salling et al.	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/051,639 Filing Date: 01/18/02	
TITLE OF INVENTION: ESD Improvement by a Vertical Bipolar Transistor with Low Breakdown Voltage and High Beta	
TI FILE NO.: TI-32536	DEPOSIT ACCT. NO.: 20-0668
FAXED: 1-9-04 DUE: --- ATTY/SECY: GCH/kjv	

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Karen Vertz  
Karen Vertz

1-9-04  
Date

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

LETTER

Based on a recent telephone discussion with the Examiner, it is understood that the above-mentioned application will be allowed and the non-elected claims will be cancelled by an Examiner's amendment.

Respectfully submitted,

Texas Instruments Incorporated  
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